

Fig. 2

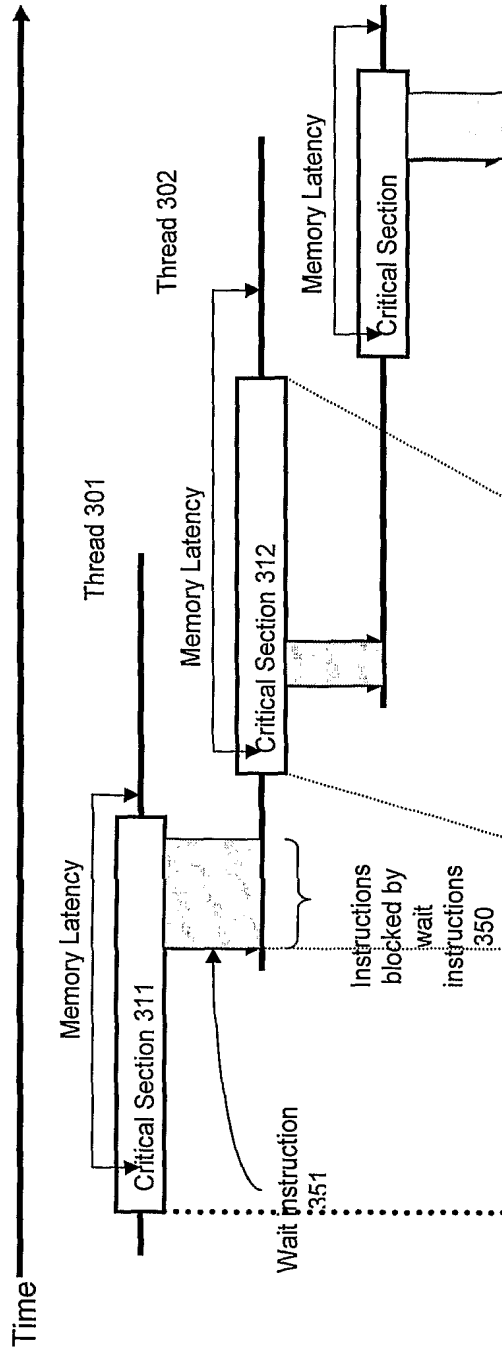


Fig. 3a

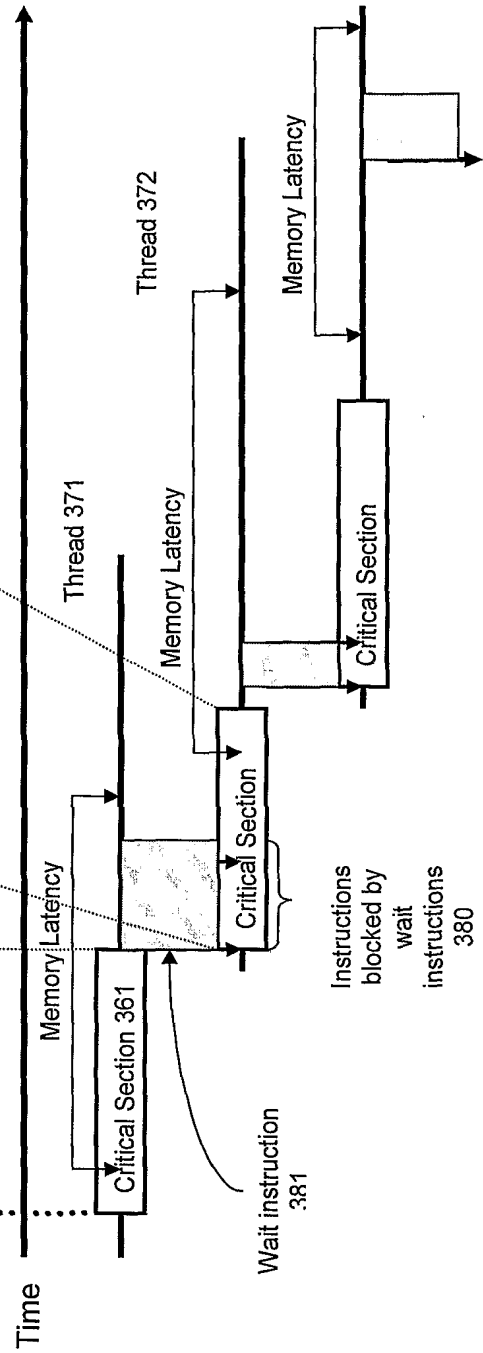
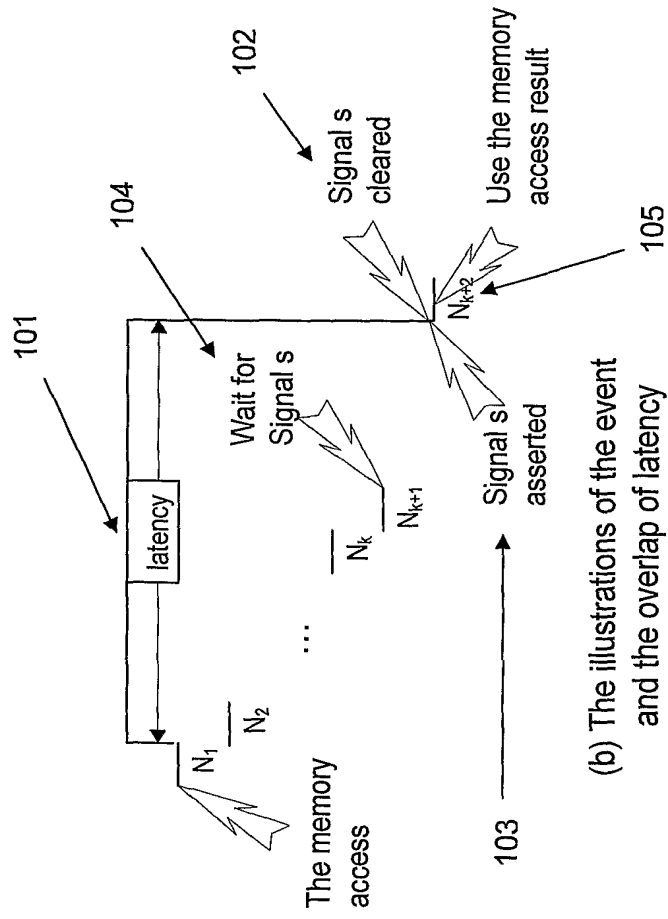


Fig. 3b



(a)

```

N1: R1 = load [R2], signal s
N2: R3 = R2 + 1
...
Nk: R4 = R3 < 2
Nk+1: wait s
Nk+2: R5 = R1 - R4

```

Figs. 1a-b

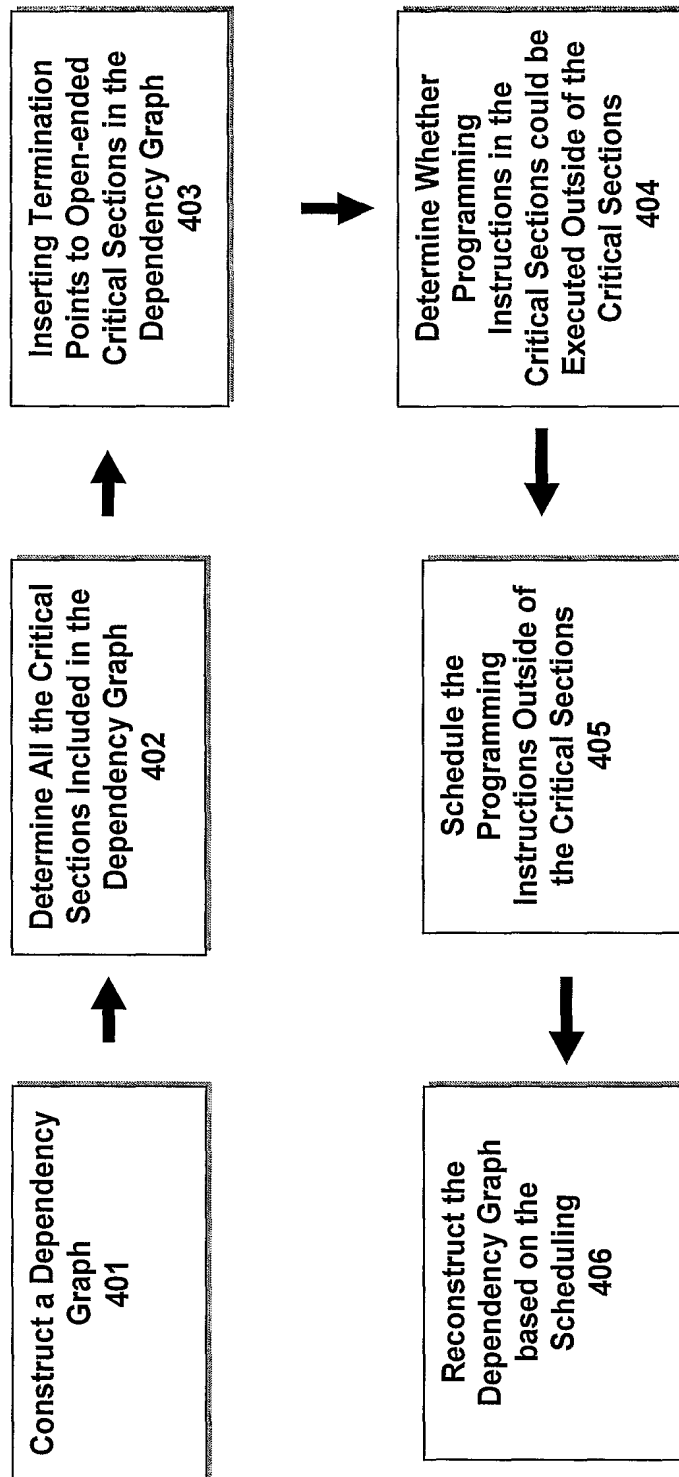


Fig. 4

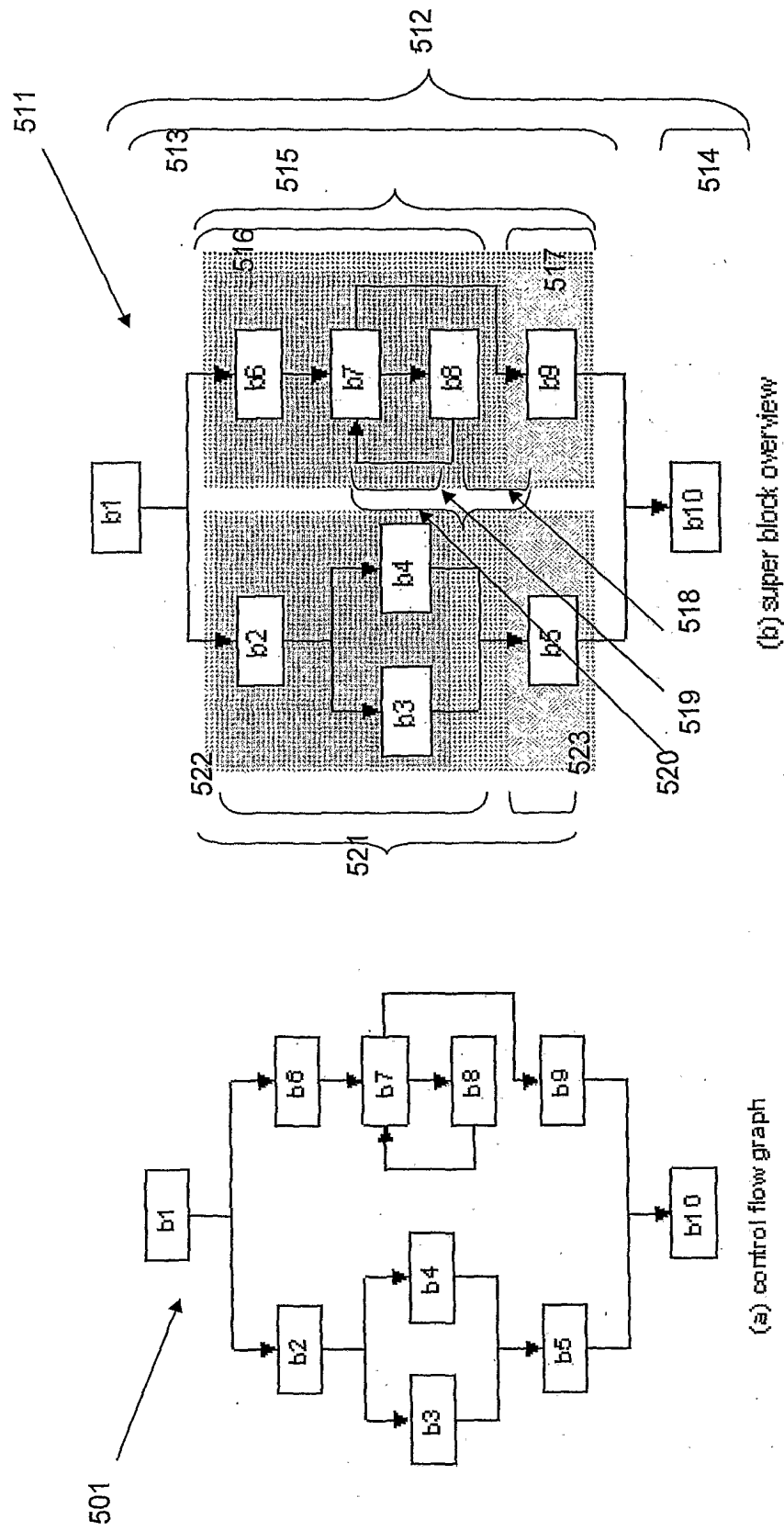
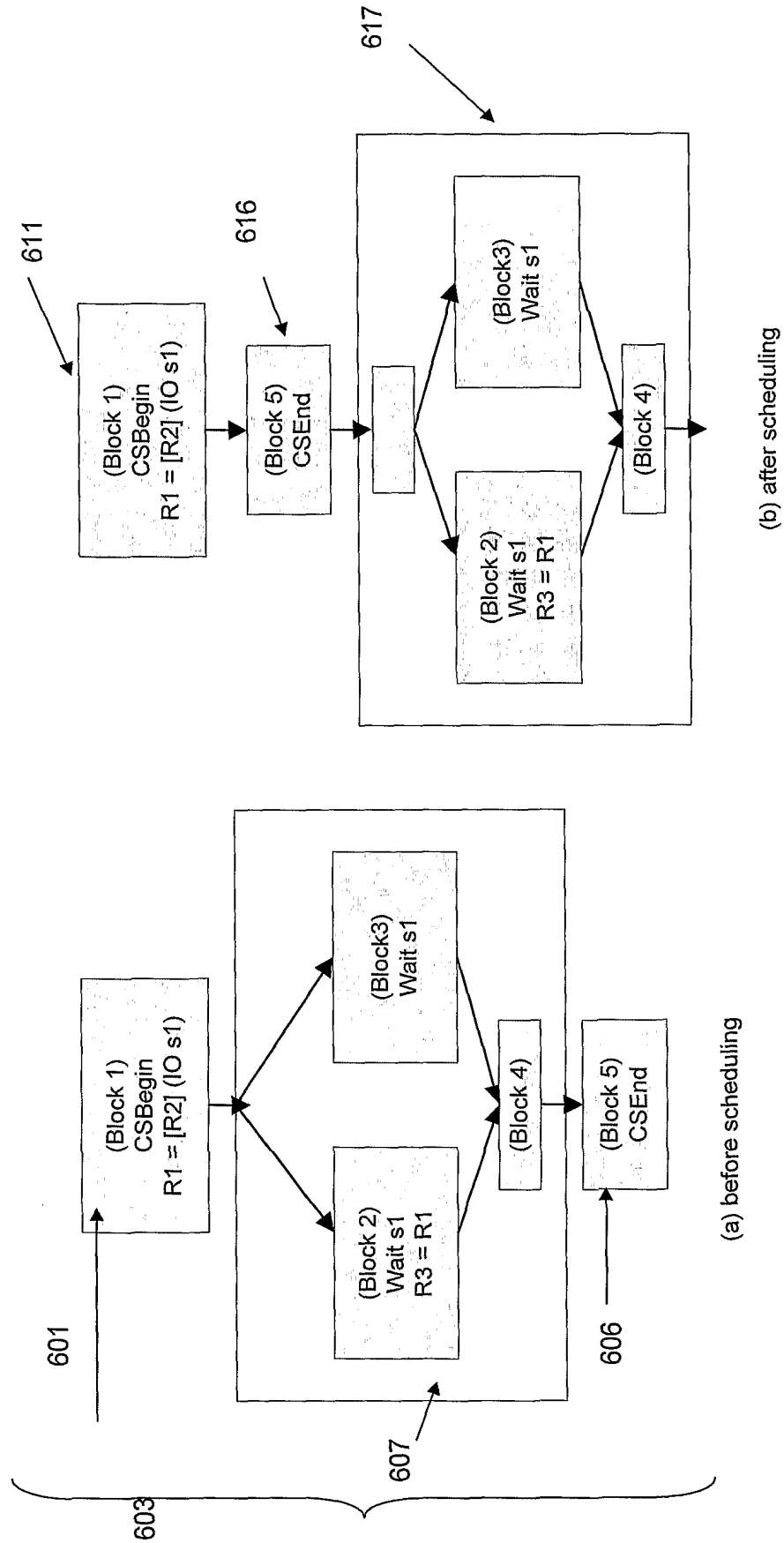


Fig. 5a

Fig. 5b

Figs. 6a-b



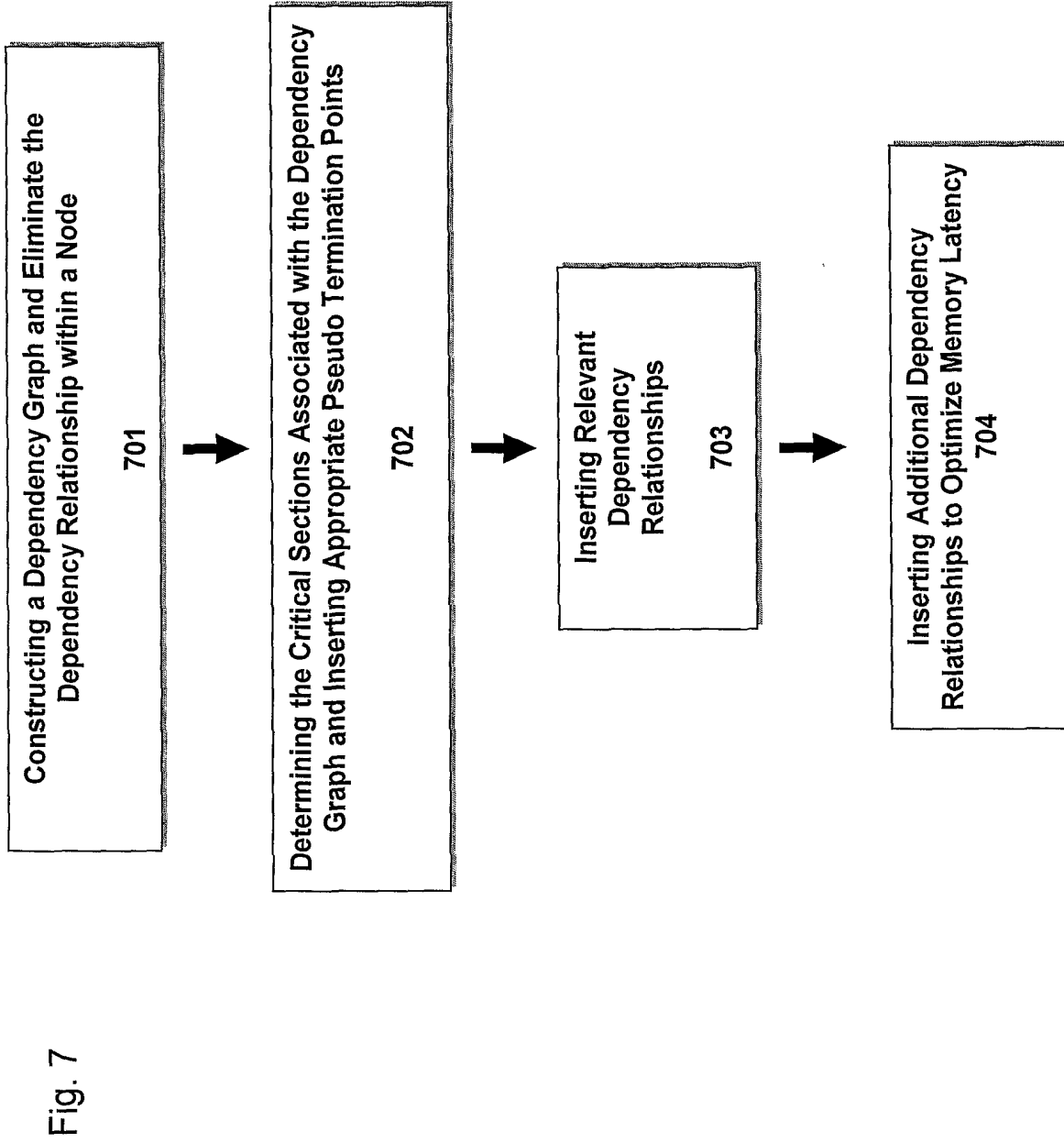
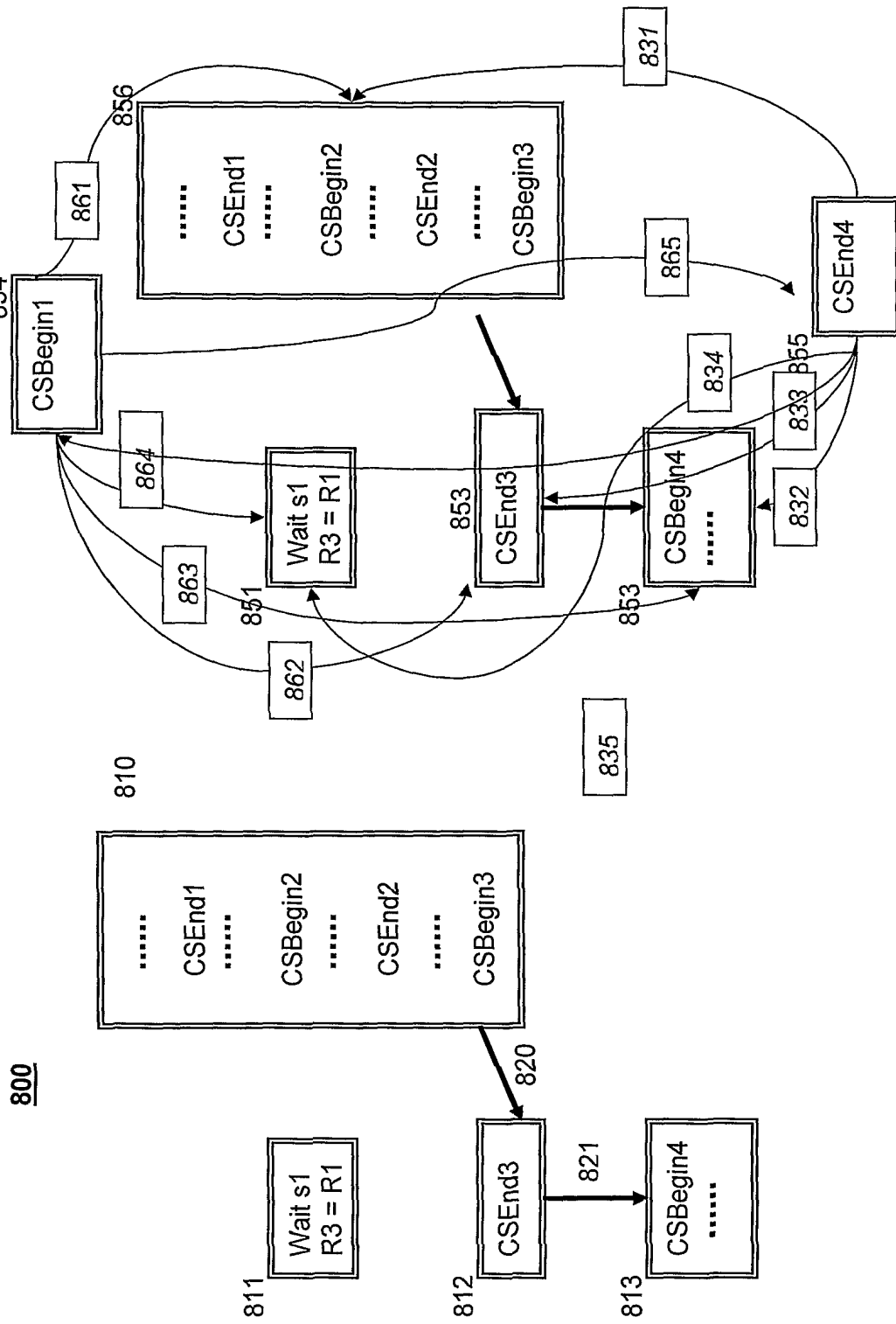


Fig. 8a



870

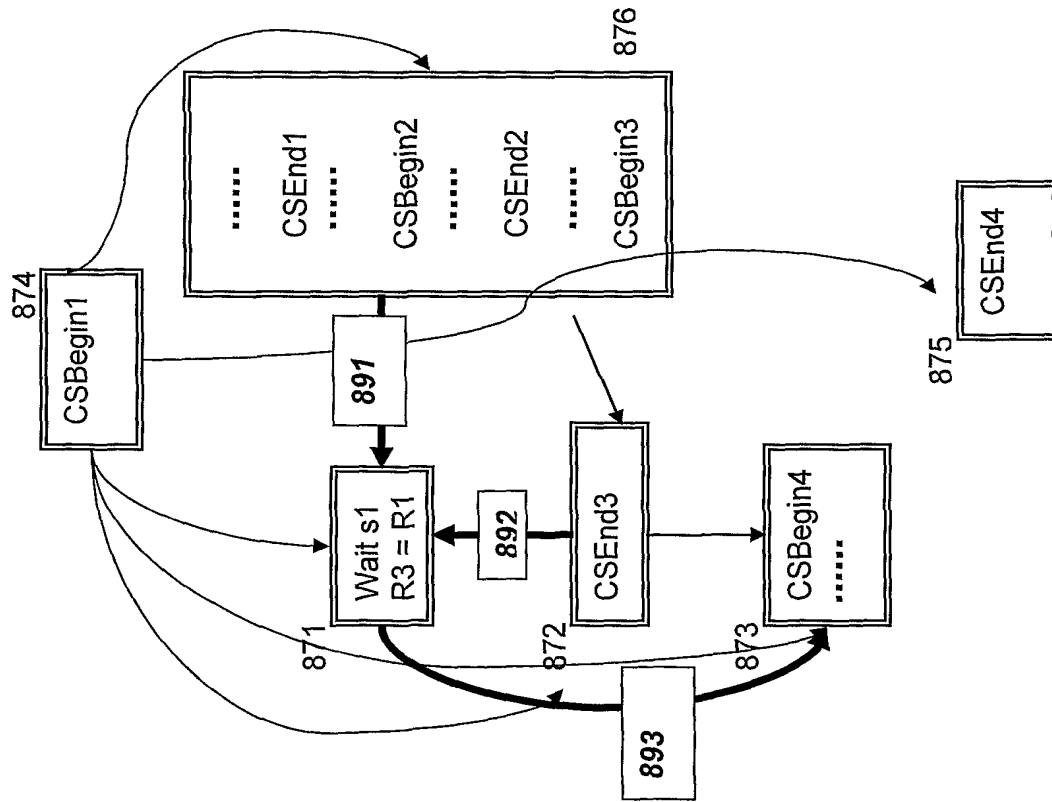


Fig. 8c